Hardware/Software Co-Programmable Framework for Computational SSDs to Accelerate Deep Learning Service on Large-Scale Graphs

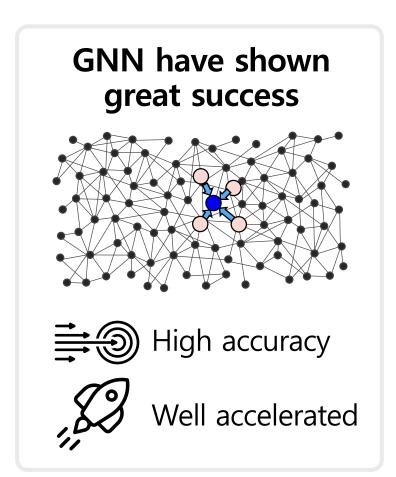
Miryeong Kwon, Donghyun Gouk, Sangwon Lee, Myoungsoo Jung

Computer Architecture and Memory systems Laboratory



First Step

High-level summary of talk







First Step

High-level summary of talk

GNN have shown great success

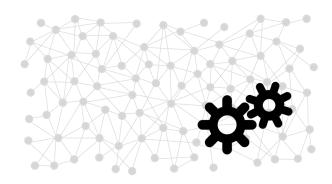


High accuracy



Well accelerated

GNN preprocessing is missed out on



Current GNN works are only focusing on GNN algorithms

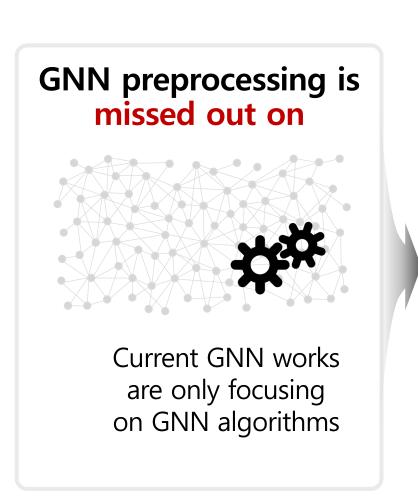


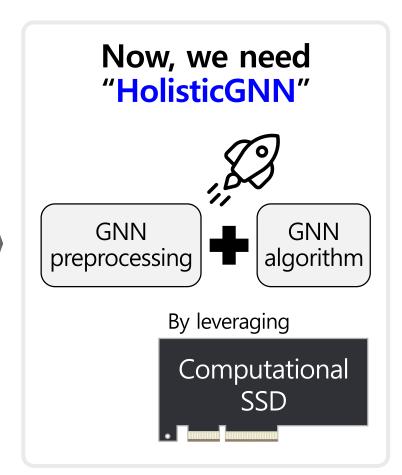


First Step

High-level summary of talk

GNN have shown great success) High accuracy Well accelerated







1. Background

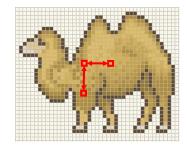
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Why is it emerging?

Conventional CNN Model



Regular data in Euclidean space (Learning information: "Euclidean distance")

Response of CNN model





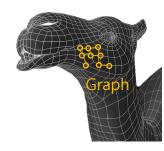
"Women near the sofa"

Query image



Characteristic: "pain"

Emerging GNN Model



Irregular data in non-Euclidean space (Learning information: "Relationship")

Response of GNN model









"pain"

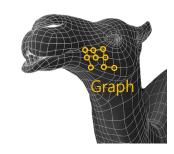


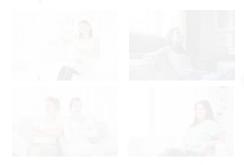


Why is it emerging?



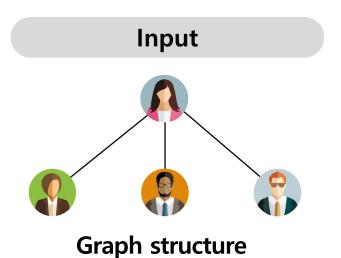
How can GNN algorithm learn the relationship?

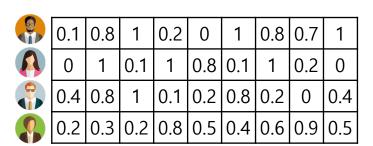












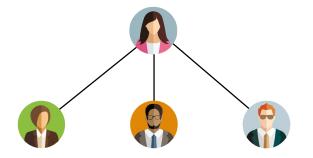
Node embedding



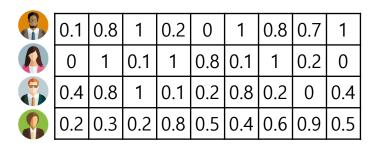


GNN algorithm



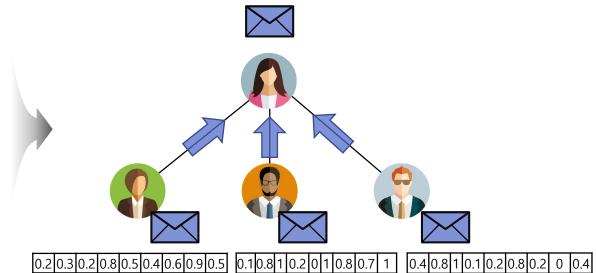


Graph structure



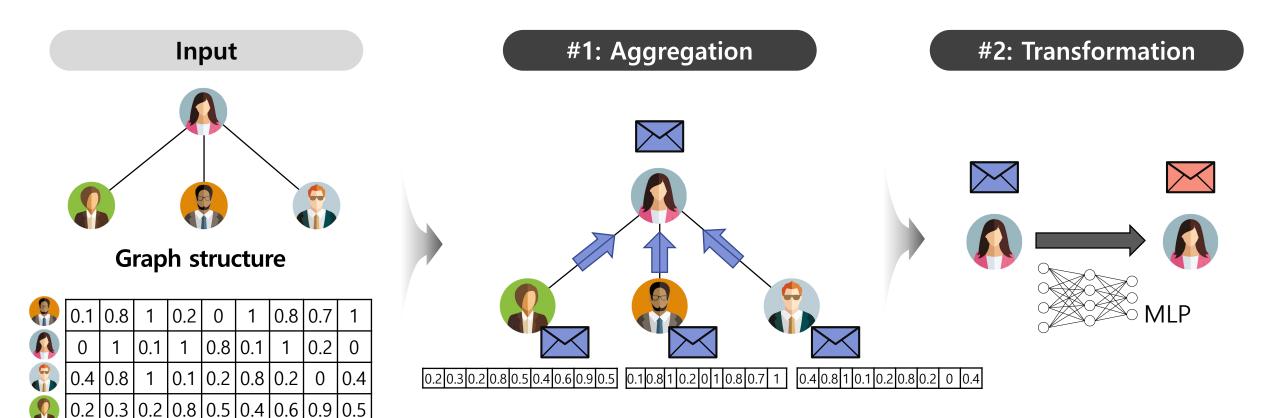
Node embedding

#1: Aggregation



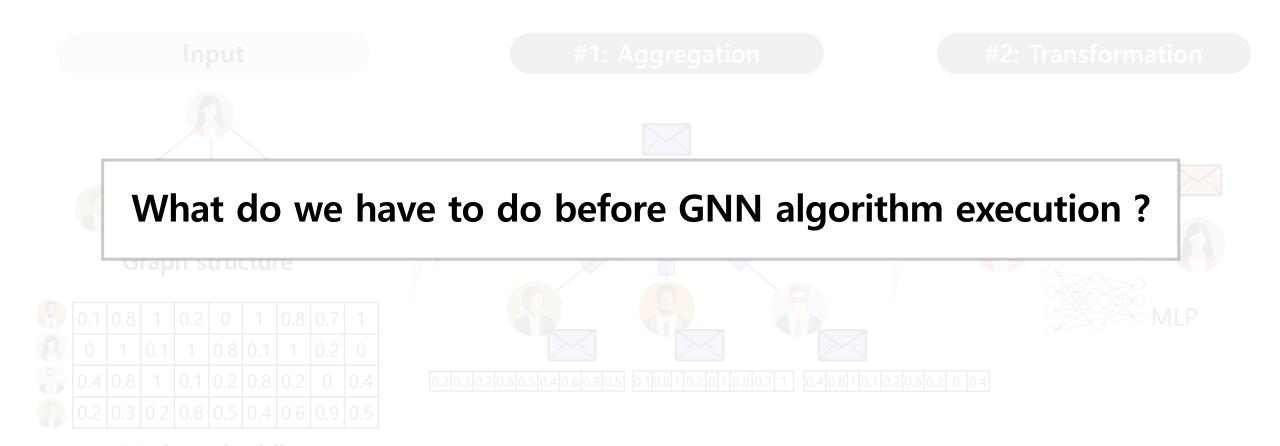




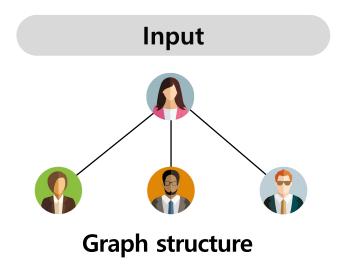


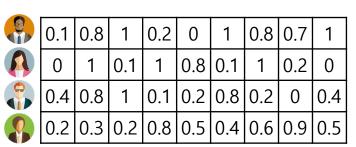




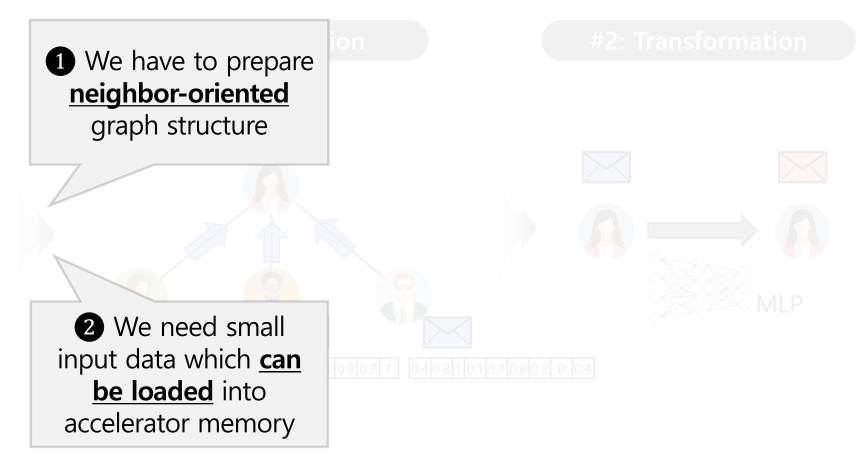








Node embedding





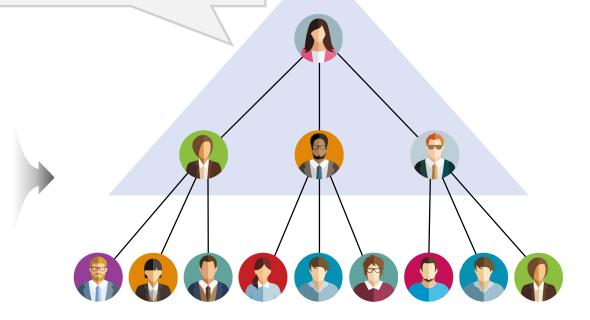
GNN Preprocessing

Graph preprocessing: to prepare neighbor-oriented graph structure

Graph structure is stored as "edge array" which is update-friendly

Graph preprocessing converts edge array to "adjacency list" which is neighbor-oriented

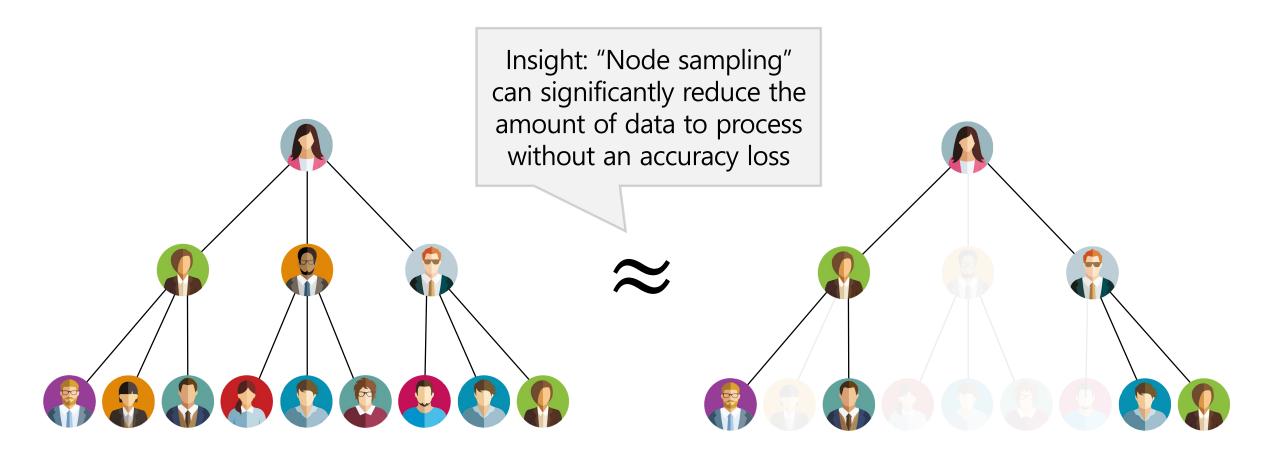






GNN Preprocessing

Batch preprocessing: to prepare small graph



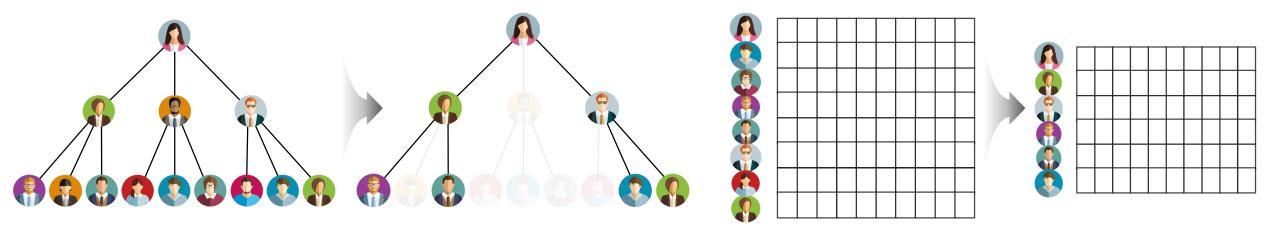


GNN Preprocessing

Batch preprocessing: to prepare small graph

Graph structure sampling

Embedding sampling







1. Background

2. Motivation and Design Considerations

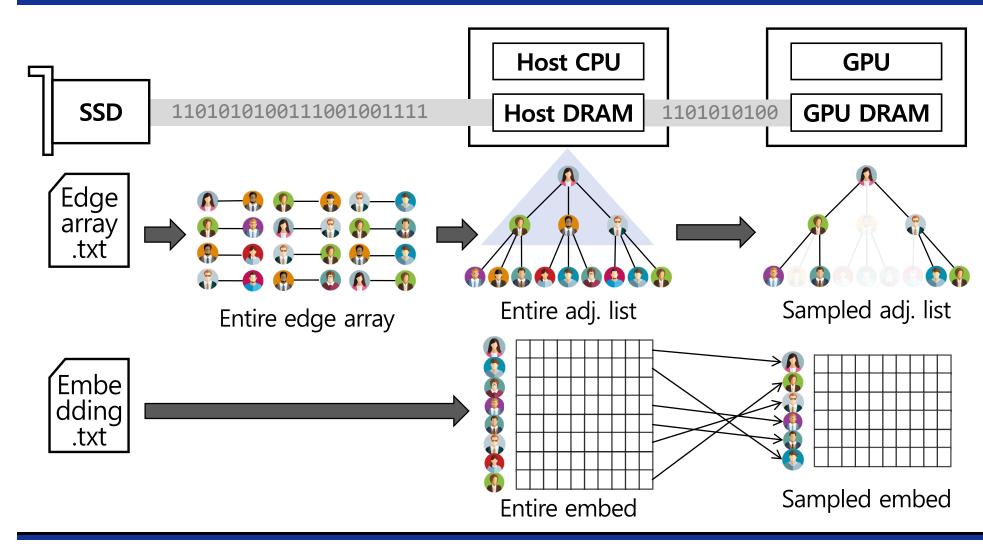
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End-to-End GNN Inference

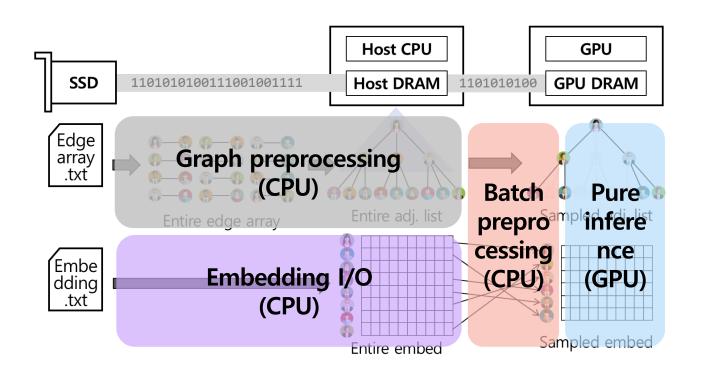
Visualization

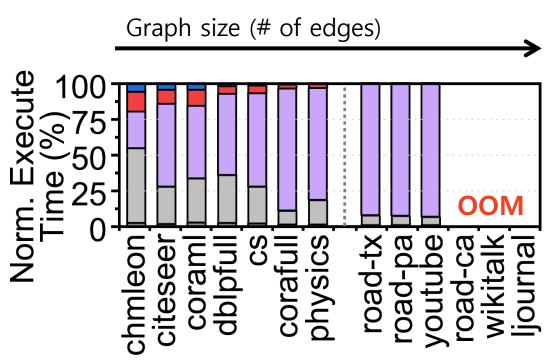




End-to-End GNN Inference

Execution time analysis



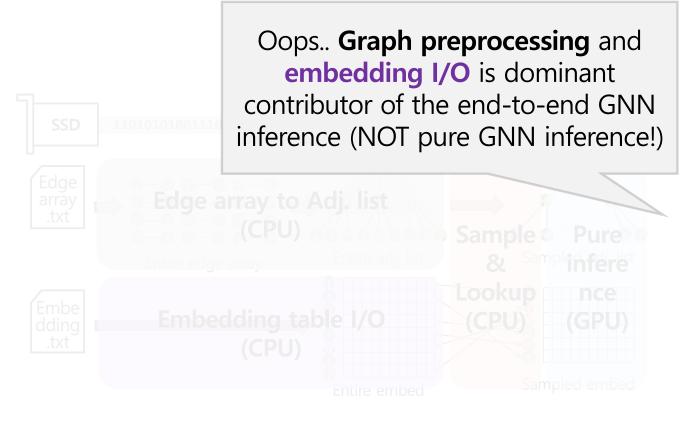


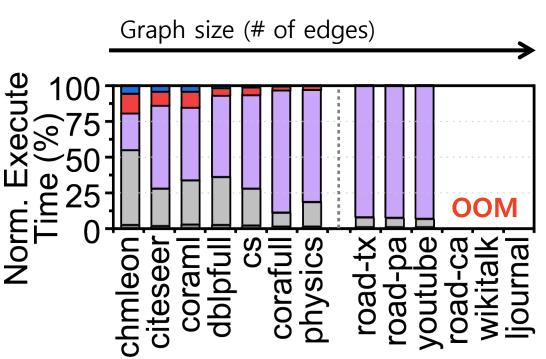




End-to-End GNN Inference

Execution time analysis







Design Questions

Then, what does GNN acceleration look like?

Graph preprocessing (CPU)



Store graph <u>directly</u> as a neighbor-oriented format (But also, update-efficient)

Embedding I/O (CPU)



Process end-to-end GNN inference <u>near storage</u>





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2. Motivation and Design Considerations

3. Overview of HolisticGNN Framework

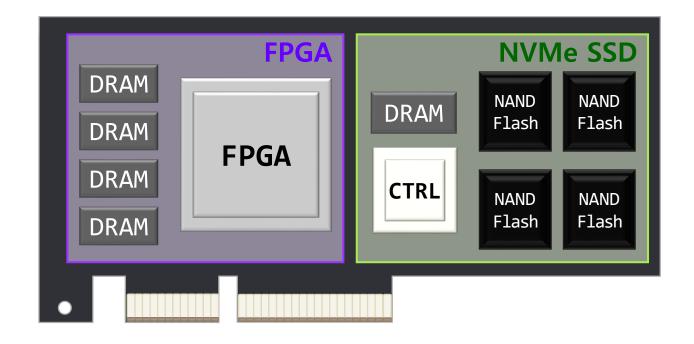
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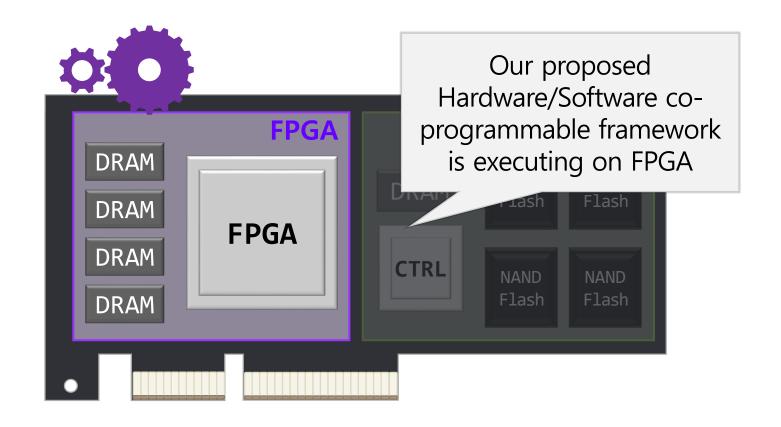
Adopts the concept of computational SSD (CSSD)



CSSD <u>decouples</u> the compute unit from the storage resources unlike conventional ISP (In-Storage Processing)

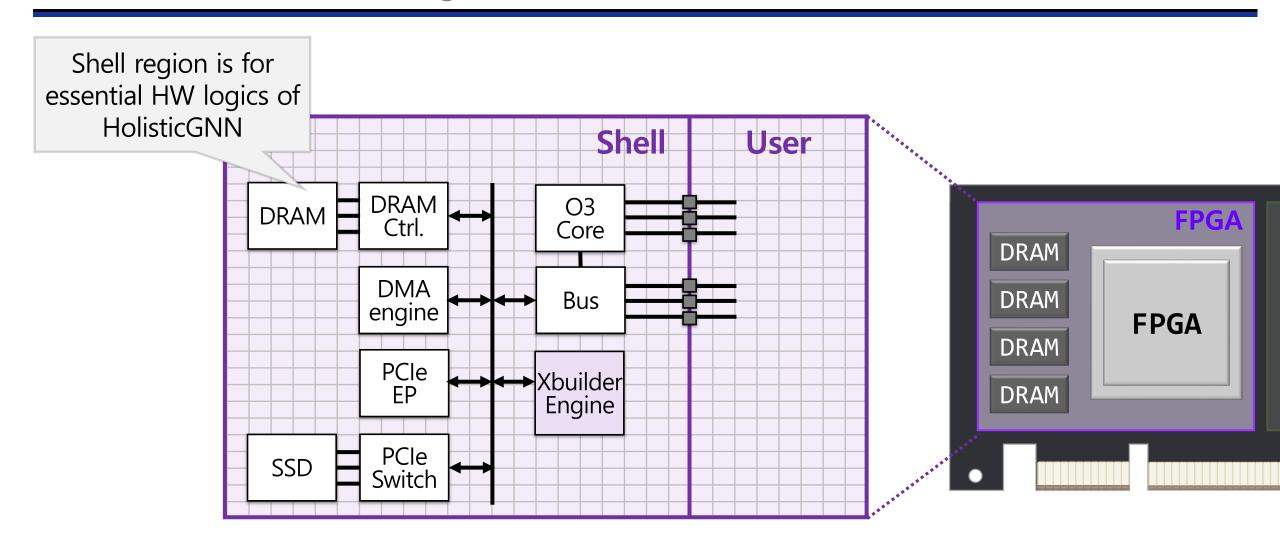






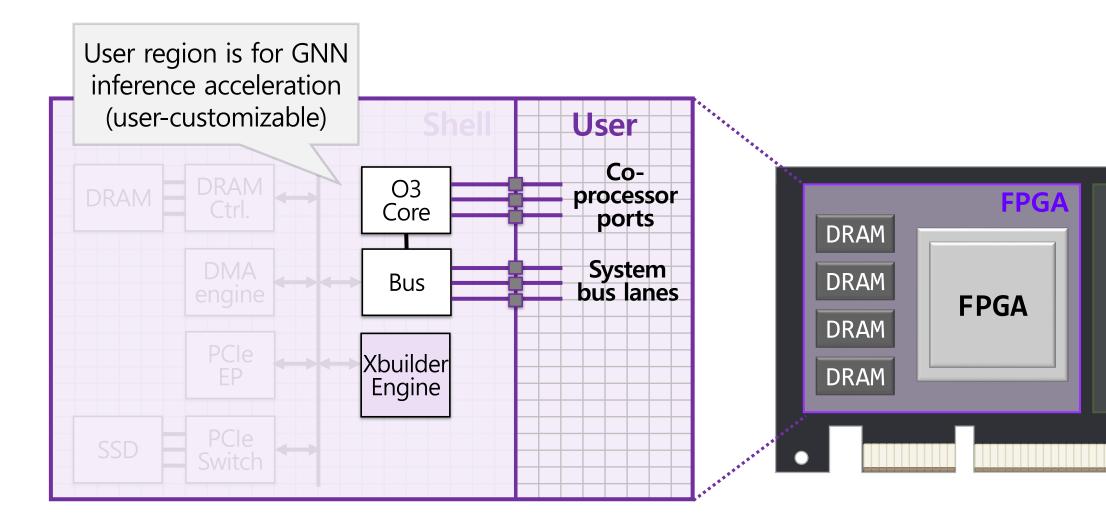






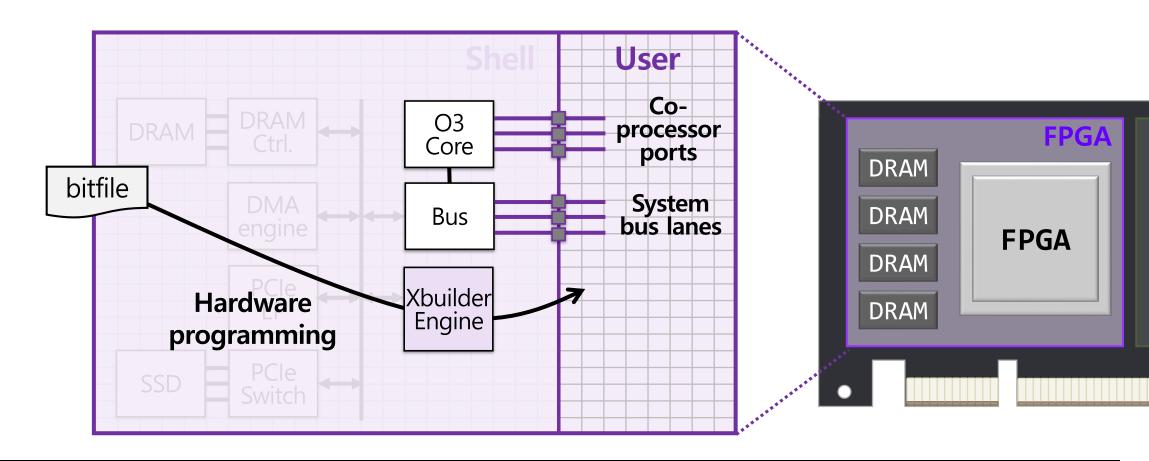






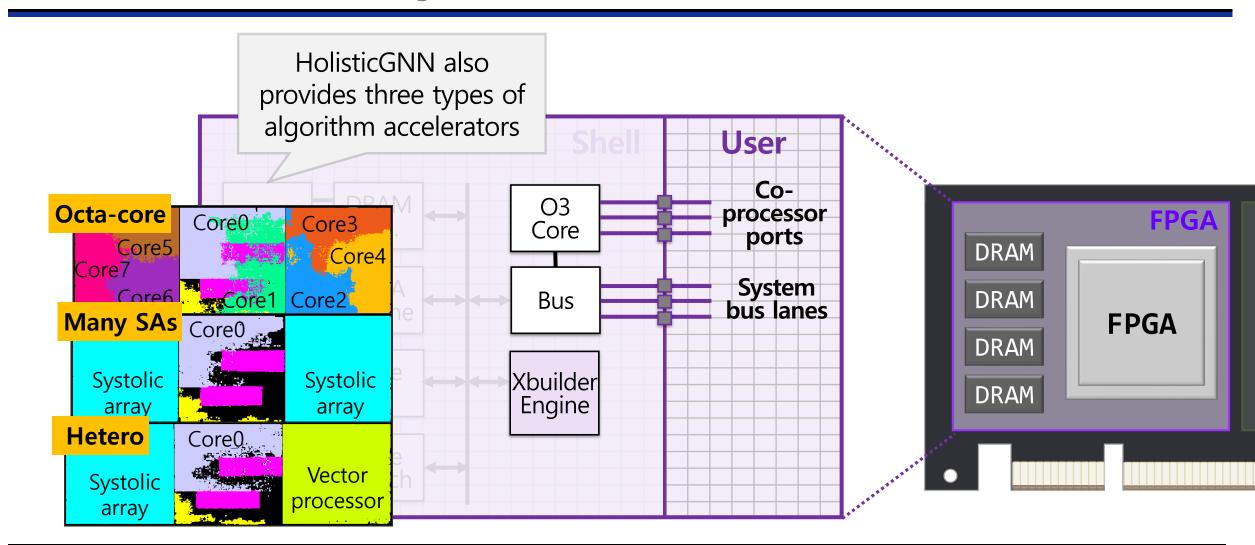






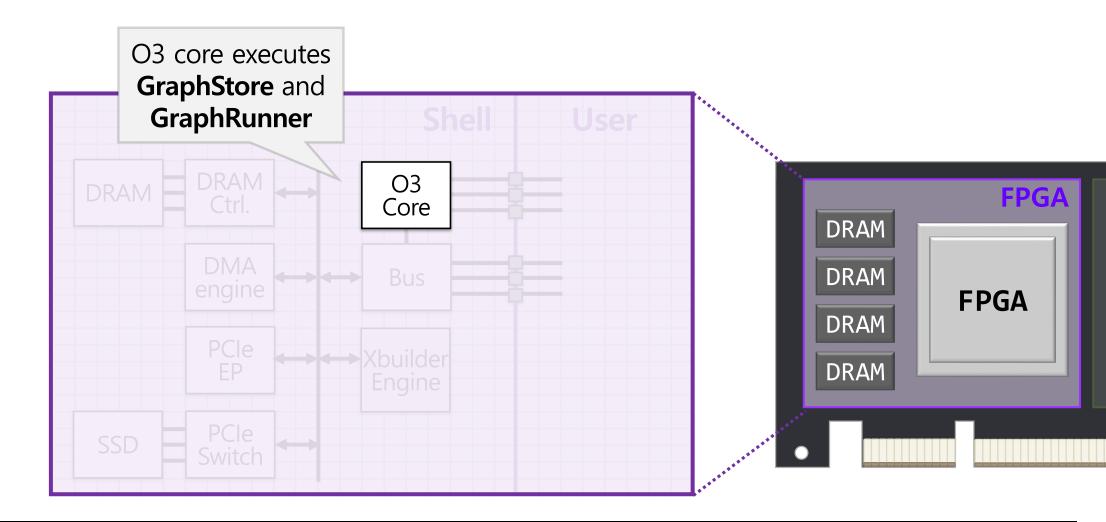






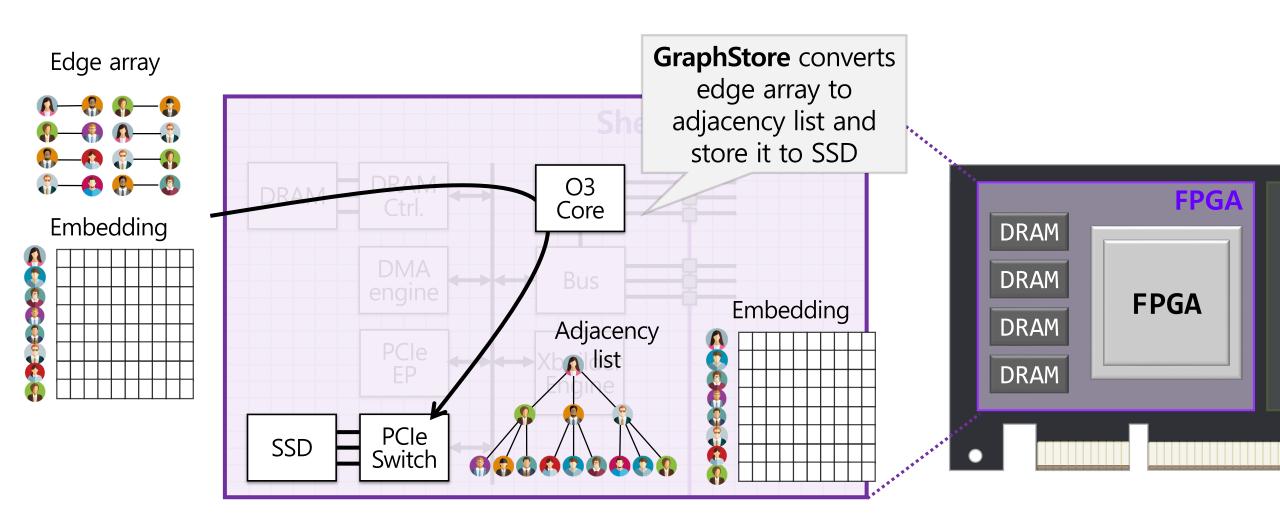






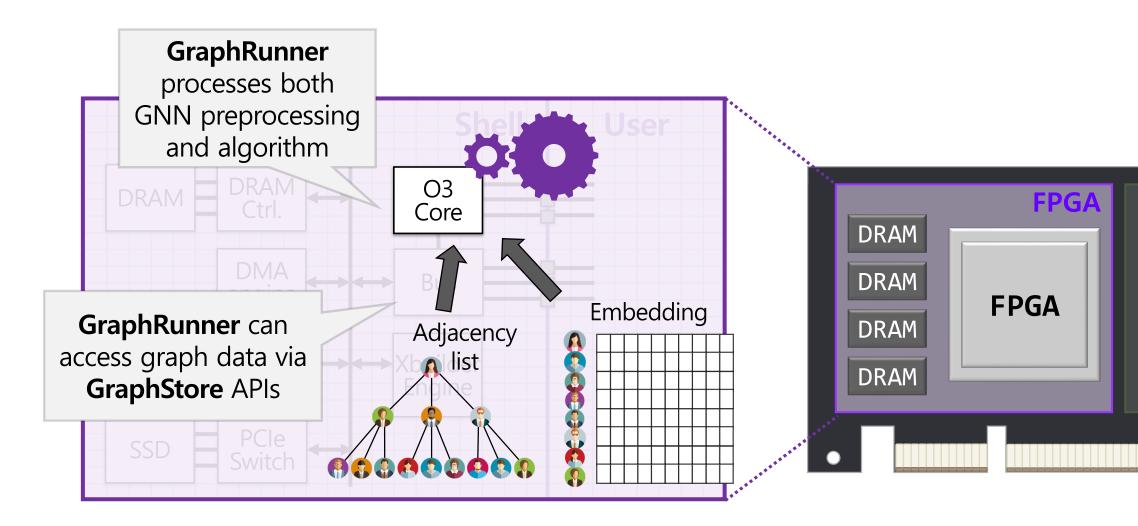
















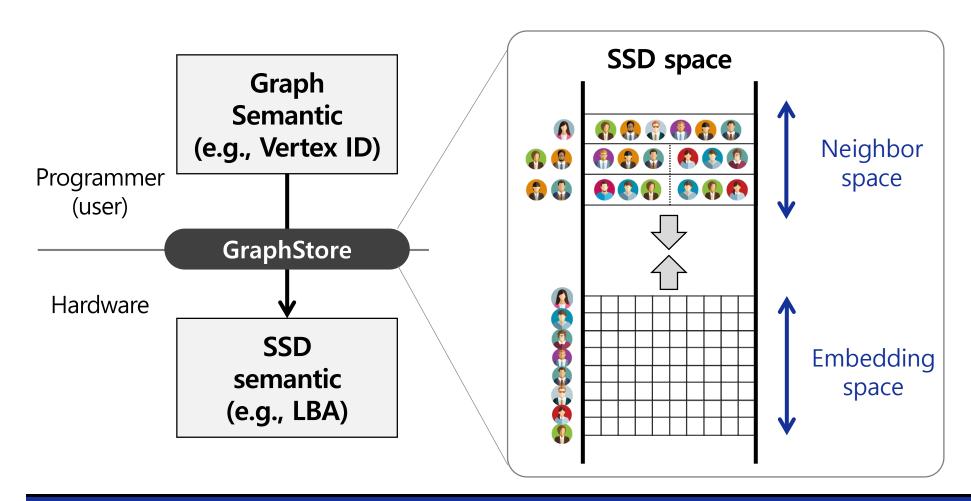
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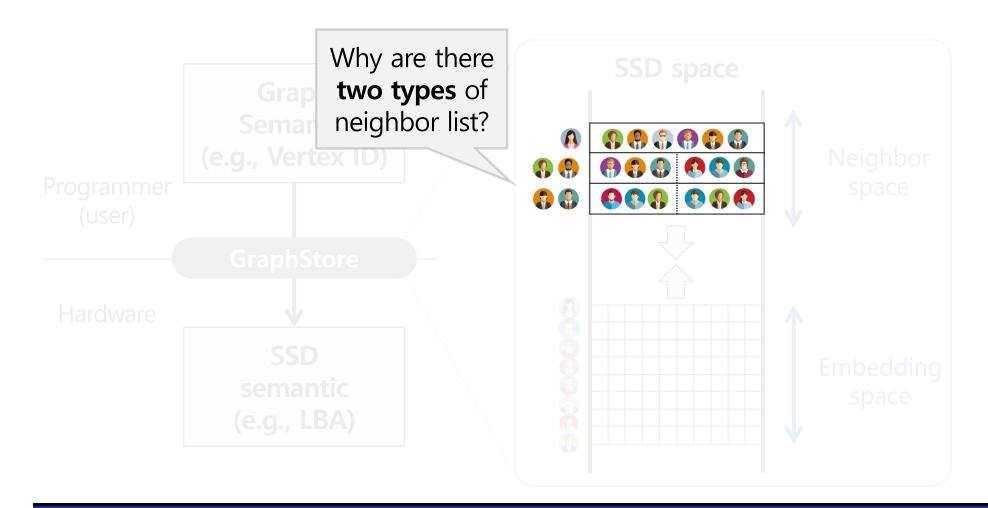
Graph-centric archiving system



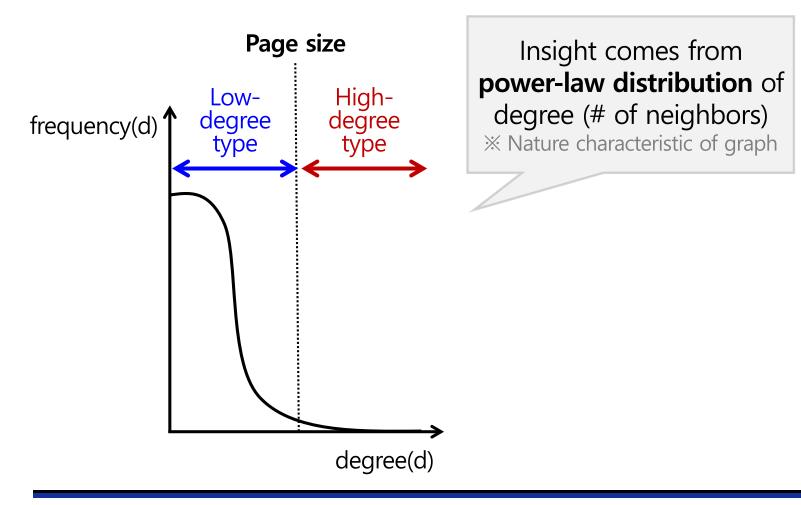
GraphStore separates SSD space into two for making sure update-efficient



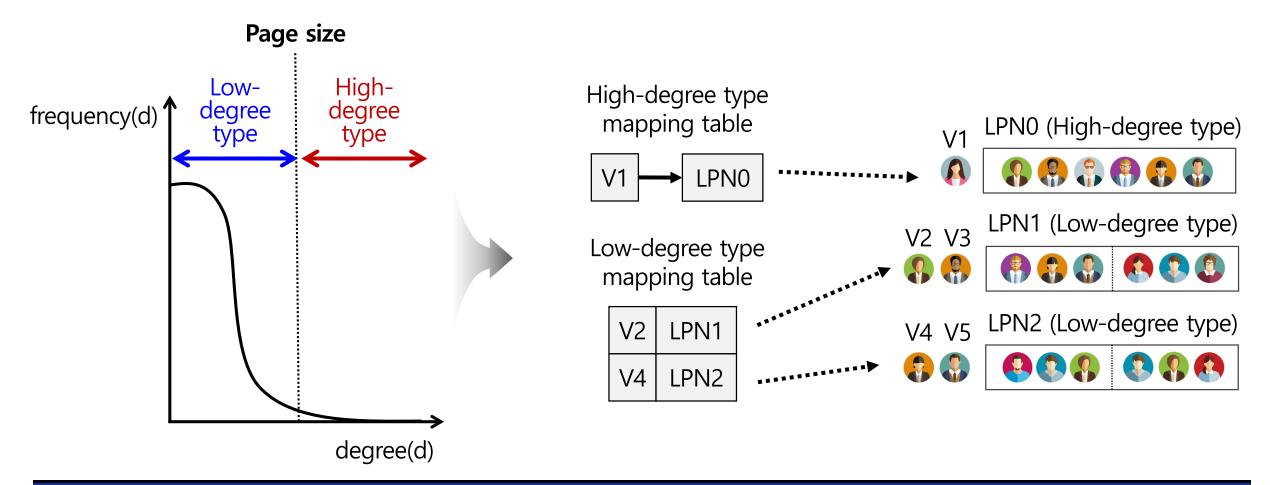




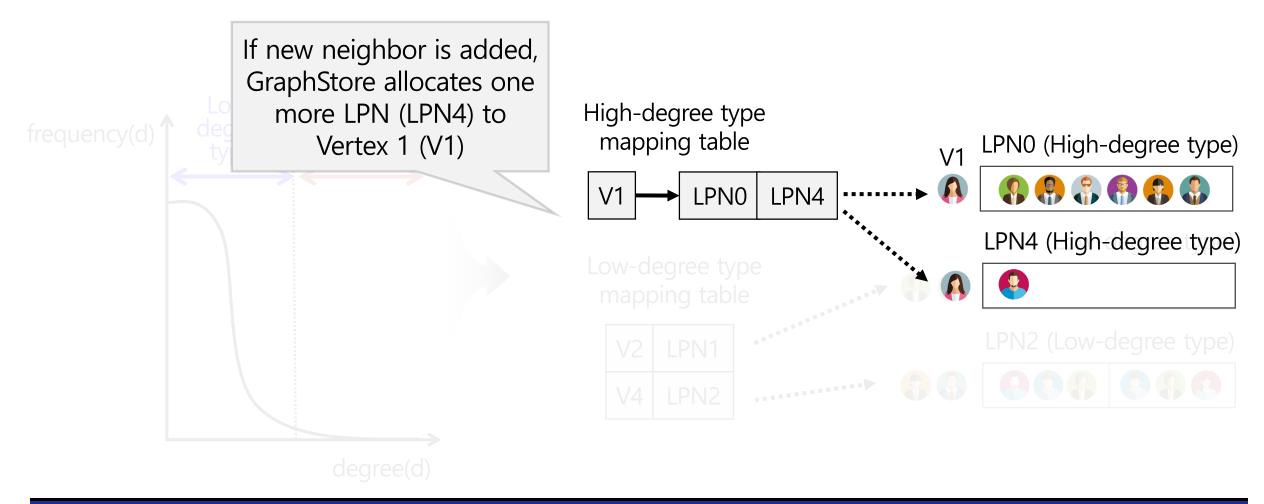








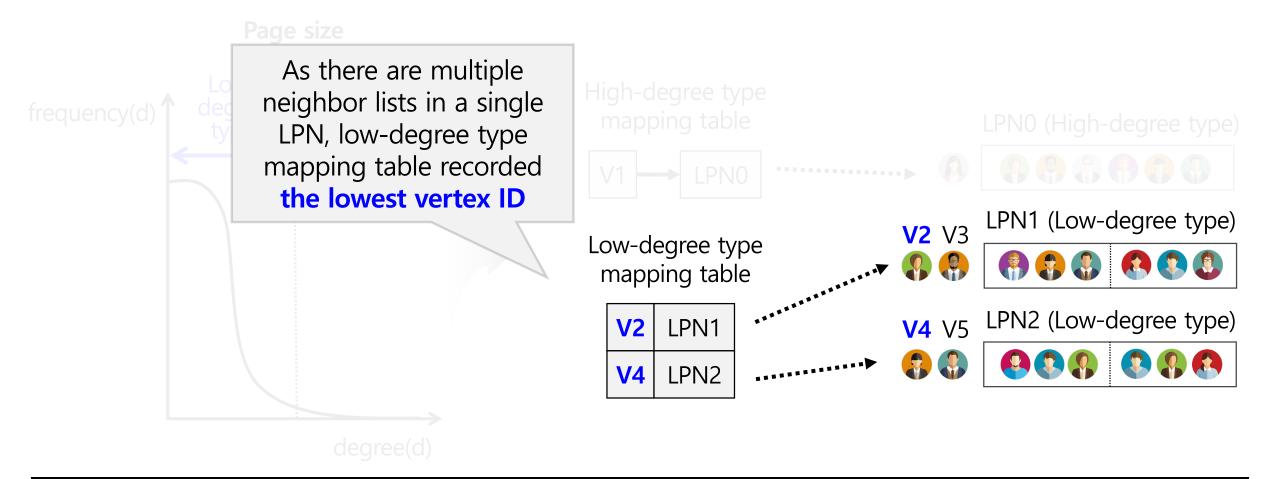






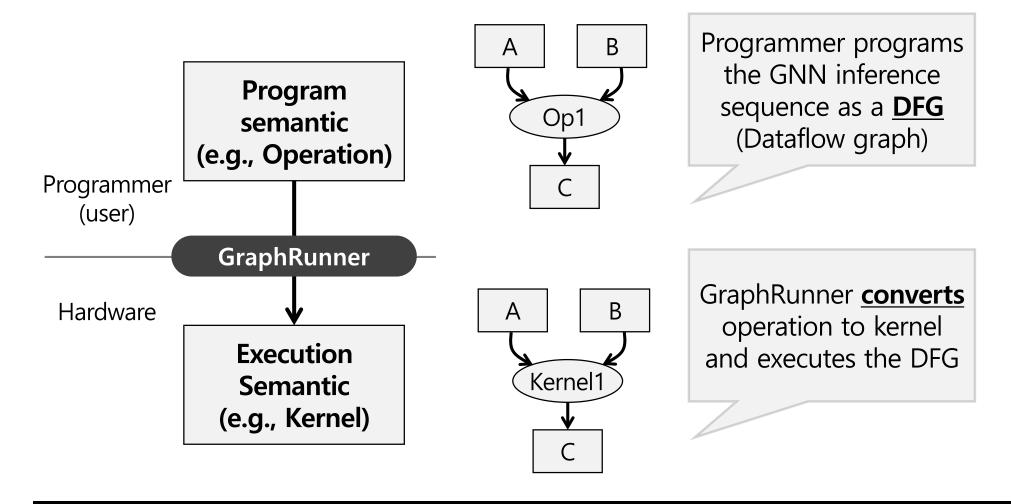
GraphStore

Graph-centric archiving system



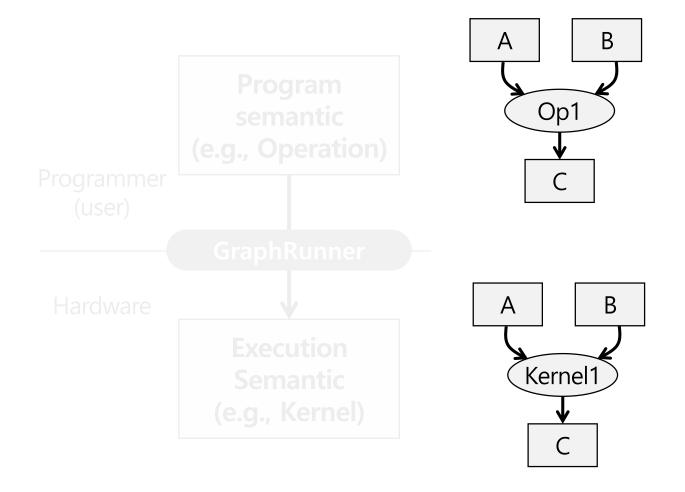


Programmable inference model





Programmable inference model



BTW, why are there **gap** between program semantic and execution semantic?





Programmable inference model

Because there are many devices which can process the same operation!

Memory space

Device table

Device Name	Priority	
"CPU"	50	
"Accelerator1"	150	
•••		

Operation table

Operation Name	Kernel	•••••
"Vector addition"	<"CPU", ptr> <"Accelerator1", ptr>	*****
		_

Vector addition (CPU code)

Vector addition (Accelerator1 code)



Programmable inference model

Users can add their own accelerator and also accelerator's kernel ©

Memory space

Device table

Device Name	Priority	
"CPU"	50	
"Accelerator1"	150	
•••		

Operation table

Operation Name	Kernel	
"Vector addition"	<"CPU", ptr> <"Accelerator1", ptr>	*****
•••	•••	

Vector addition (CPU code)

Vector addition (Accelerator1 code)

Vector addition (Accelerator2 code)





1. Background

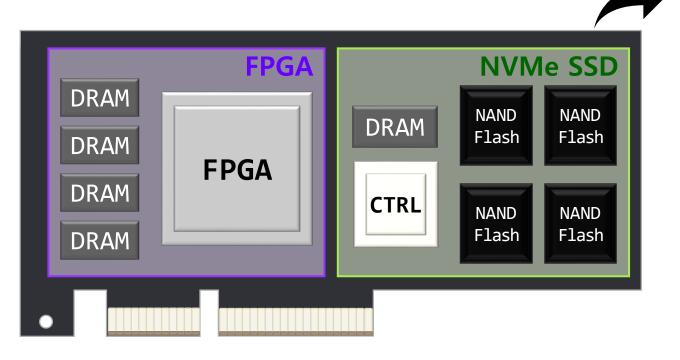
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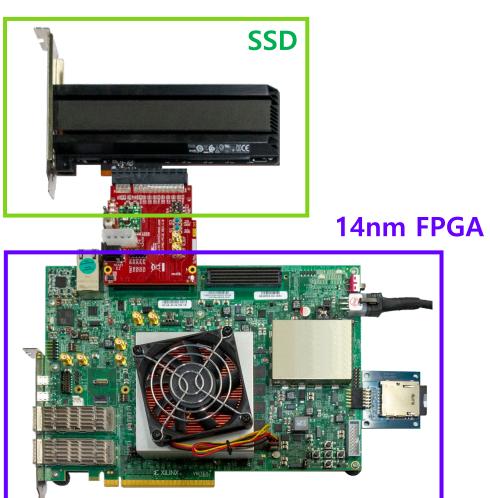




Experimental Setup

HolisticGNN prototype







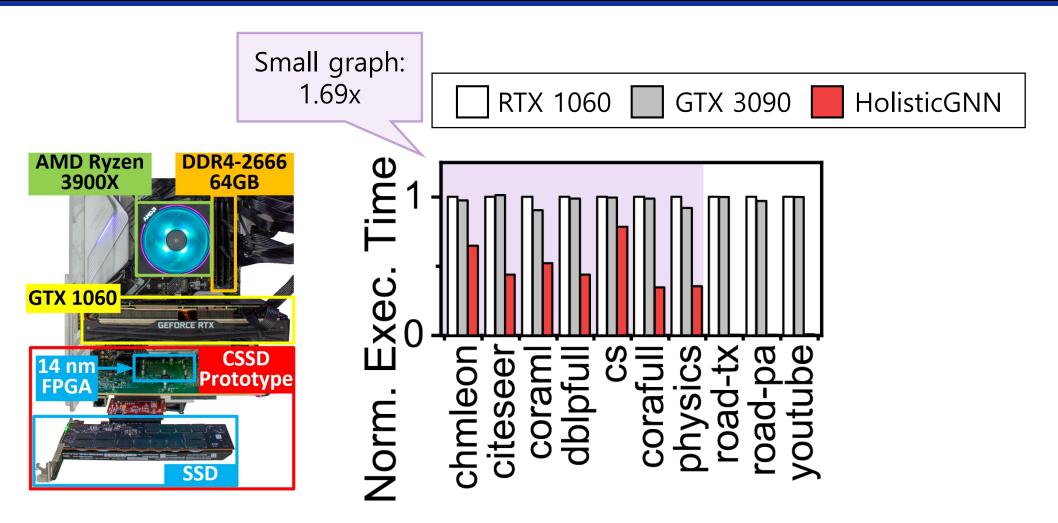
Experimental SetupGraph dataset

		Vertices	Edges	Feature length
Small (<1M Edge)	chmleon	2.3K	65K	2326
	citeseer	2.1K	9K	3704
	cormal	3.0K	19K	2880
	dblpfull	17.7K	123K	1639
	CS	18.3K	182K	6805
	corafull	19.8K	147K	8710
	physics	34.5K	530K	8415
Large (>3M Edge)	road-tx	1.39M	3.84M	4353
	road-pa	1.09M	3.08M	4353
	youtube	1.16M	2.99M	4353
	road-ca	1.97M	5.53M	4353
	wikitalk	2.39M	5.02M	4353
	ljournal	4.85M	68.99M	4353





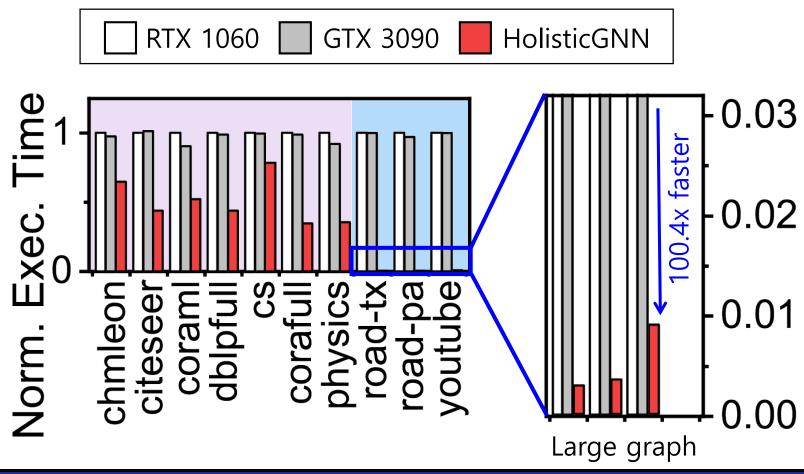
End-to-End latency comparison





End-to-End latency comparison





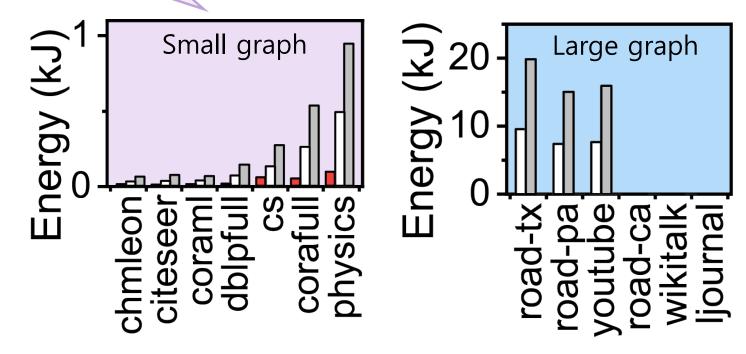




Energy Consumption

33.2x and 16.3x better than GTX 3090, RTX 1060

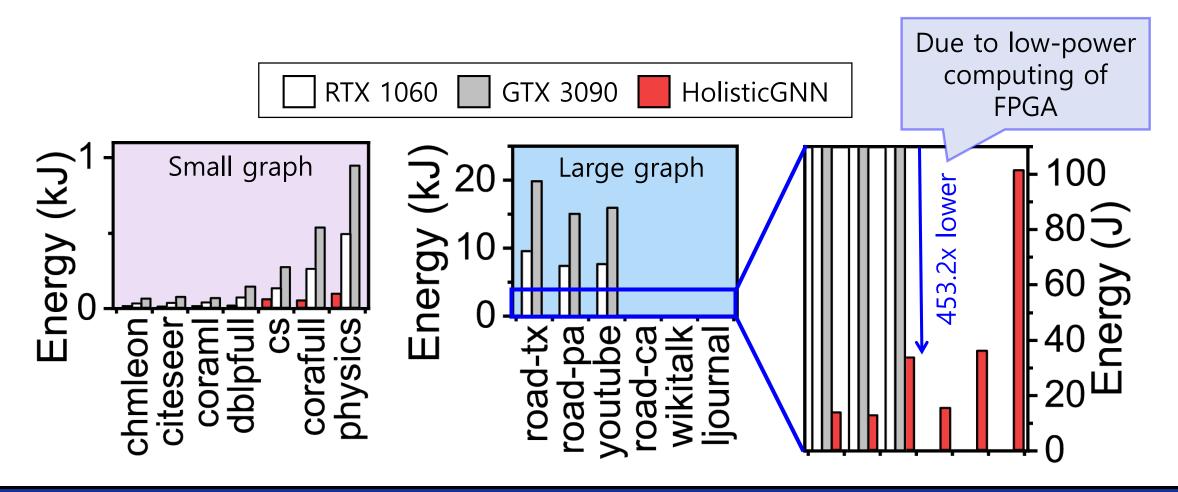








Energy Consumption







Demo

GNN execution in our HolisticGNN prototype





Conclusion

HolisticGNN is a "hardware/software co-programmable framework for computational SSDs"

- 1) Holistic solution for both GNN algorithm and preprocessing
- 2) Fast and energy-efficient near-storage inference infrastructure
- 3) Easy-to-use and user-customizable





Thank You

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