Multi-Dimensional Spatially-Coupled Code Design with Improved Cycle Properties

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Abstract—A spatially-coupled (SC) code is constructed by coupling disjoint block codes into a single coupled chain. By connecting (coupling) several SC codes, multi-dimensional SC (MD-SC) codes are constructed. In this work, we present a systematic framework for constructing MD-SC codes with notably better cycle properties than the 1D-SC counterparts. Compared to the 1D-SC codes, our MD-SC codes are demonstrated to have up to 85% reduction in the population of the smallest cycle, and up to 3.8 orders of magnitude BER improvement in the early error floor region. The results of this work can be particularly beneficial in data storage systems, e.g., 2D magnetic recording and 3D Flash systems, as high-performance MD-SC codes are robust against various channel impairments and non-uniformity.

I. INTRODUCTION AND PRELIMINARIES

Spatially-coupled (SC) codes are a family of graph-based codes that have attracted significant attention thanks to their capacity approaching performance [2]. Multi-dimensional SC (MD-SC) codes are constructed by coupling several SC codes together via rewiring the existing connections or by adding extra variable nodes (VNs) or check nodes (CNs) [3], [4]. Previous works on MD-SC codes, while promising, have some limitations. In particular, they either consider random constructions, are limited to specific topologies, or consider the asymptotic behaviour, e.g. [3]–[6].

MD-SC codes are more robust against burst erasures and channel impairments, e.g., multi-dimensional inter symbol interference (ISI) that occur in 2D magnetic recording and 3D Flash systems. We present a systematic framework for constructing MD-SC codes by optimally coupling individual SC codes together to attain fewer short cycles, and consequently better performance. For exchanging the connections, we follow three rules: (1) The connections involved in the highest number of certain cycles are targeted for rewiring; (2) The neighboring constituent 1D-SC code to which the targeted connections are rewired is chosen such that the number of the certain cycles is attained; (3) The targeted connections are rewired to the same positions in the other constituent 1D-SC codes in order to preserve the low-latency decoding property.

The parity-check matrix \( H \) of a regular circulant-based (CB) code consists of \( \kappa \gamma \) circulants, where \( \gamma \) is the column weight of the parity-check matrix and \( \kappa \) is the row weight. Each circulant is a \( z \times z \) identity matrix cyclically shifted some units to the left. We use CB codes as the underlying block codes to construct SC codes. The parity-check matrix \( H_{SC} \) of a CB SC code is constructed as follows: First, the circulants of \( H \) are partitioned into \( (m + 1) \) disjoint component matrices (of the same size as \( H \)): \( H_0, H_1, \ldots, H_m \). Second, \( L \) copies of the component matrices are pieced together in a diagonal structure to obtain \( H_{SC} \) (see [7]). The parameters \( m \) and \( L \) are called the memory and the coupling length, respectively. A replica \( R_d, 1 \leq d \leq L \), is a group of consecutive columns in \( H_{SC} \) that include one instance of each component matrix. We use the OO-CPO technique for designing SC codes [7]. In this work, we present a systematic framework to construct MD-SC codes, which is based on an optimal relocation of circulants to obtain notably lower population of short cycles (short cycles have a negative impact on the performance of graph-based codes).

II. NOVEL FRAMEWORK FOR MD-SC CODE DESIGN

Consider three instances of an SC code with parity-check matrix \( H_{SC} \), memory \( m \), and coupling length \( L \). Consider the middle replica \( R_d \) in \( H_{SC} \), where \( d = \lfloor L/2 \rfloor \). Out of the non-zero circulants of this replica, we choose \( T \) circulants that are the most problematic, i.e., that contribute to the highest number of cycles-\( k \), where \( k \) is the girth of the SC code. We relocate the chosen circulants to two auxiliary matrices, \( P \) and \( Q \), such that a relocated circulant from \( H_{SC} \) is moved to the same position in either \( P \) or \( Q \). The same relocations are repeated for all the \( (L - 1) \) remaining replicas. These auxiliary matrices have the same dimensions as \( H_{SC} \), and

\[
H_{SC} = H_{sc}' + P + Q,
\]

where \( H_{SC}' \) is derived from \( H_{SC} \) by removing the \( T \) chosen circulants. The parity-check matrix of the MD-SC code, \( H_{MD}^{SC} \), is then constructed as follows:

\[
H_{MD}^{SC} = \begin{bmatrix}
H_{SC} & Q & P \\
Q & H_{SC}' & Q \\
P & H_{SC}' & Q
\end{bmatrix}.
\]

Fig. 1 shows the graphical illustration of an MD-SC code having the presented structure. In our new MD-SC code design framework, we effectively answer two questions: which circulants to relocate, and where to relocate them. To this end, we first investigate the effect of relocating a subset of circulants of a cycle, and we call this subset targeted circulants. As we show, some relocations remove a cycle from \( H_{MD}^{SC} \), while others preserve the cycle.

**Theorem 1.** Let \( C_{G_k} = \{ C_{i_1}, C_{i_2}, \ldots, C_{i_k} \} \) be the sequence of circulants in \( H_{SC} \) that are visited in a clockwise order by
a cycle-\(k\) (a cycle with length \(k\)), and let \(M : \{C_i\} \rightarrow \{0, 1, 2\}\) be a mapping from a non-zero circulant in \(H_{SC}\) to an integer in \(\{0, 1, 2\}\) such that the values 0, 1, and 2 correspond to “no relocation”, “relocation to \(P\)”, and “relocation to \(Q\)”, respectively. If the following equation holds, three instances of the cycle, in the three constituent SC codes, are preserved in the MD-SC code,\[ \sum_{u=1}^{k} (-1)^u M(C_{i_u}) = 0 \pmod{3}. \tag{3} \]

Otherwise, three instances of the cycle, in three constituent SC codes, form a cycle of length \(3k\) in the MD-SC code.

**Proof.** Proof is given in [1].

Next, we briefly explain our new systematic framework for constructing MD-SC codes which is based on a majority voting policy and aims at minimizing the population of cycles-\(k\). Initially, \(H_{SC} = H_{SC}^0\) and \(P = Q = 0\). At each iteration, one non-zero circulant in replica \(R_d\) of \(H_{SC}\), \(d = \lfloor L/2 \rfloor\), which is the circulant involved in the highest number of cycles-\(k\) in \(H_{MD}^0\), is targeted for relocation. Each cycle \(O_k\) in \(H_{SC}\) that has the targeted circulant in its sequence votes for a subset of actions \{relocate to \(P\), relocate to \(Q\), keep in \(H_{SC}\)\} that remove instances of \(O_k\) in the MD-SC code, i.e., actions that break the condition in (3). The decision is made based on the majority of the votes. The same decision applies to the other \((L-1)\) instances of this circulant in \(H_{SC}\). The relocations are performed sequentially until the MD threshold is achieved, or relocation does not help anymore.

As the final (optional) step, a post-processing circulant power optimizer (PP CPO) is performed to remove as many as possible of remaining cycles-\(k\) in \(H_{SC}^{MD}\) by adjusting the powers of the relocated circulants.

### III. Simulation Results

This work presented the first systematic framework for constructing MD-SC codes with better cycle properties, and AWGN channel is considered as the first step in the simulation results. One promising research direction is to investigate MD-SC codes on non-uniform channels, such as multilevel Flash.

Our code parameters are as follows: SC-Code-1 and SC-Code-2 are 1D-SC codes that are used as constituent SC codes. SC-Code-1 has column weight 4, length 2,890 bits, rate 0.74, and girth 6. SC-Code-2 has column weight 3, length 4,370 bits, rate 0.81, and girth 8. MD-SC-Code-1 and MD-SC-Code-2 are MD-SC codes with SC-Code-1 and SC-Code-2 as their constituent SC codes, and their MD densities are \(T_1 = 15\) (i.e., 22.06%) and \(T_2 = 12\) (i.e., 21.05%), respectively. MD-SC-Code-3 is constructed from MD-SC-Code-2 by applying the optional PP CPO step. MD-SC-Code-1 has length 8,670 bits and rate 0.74. MD-SC-Code-2 and MD-SC-Code-3 have length 13,110 bits and rate 0.81. SC-Code-3 (resp., SC-Code-4) is similar to SC-Code-1 (resp., SC-Code-2), but with larger coupling length, and it has comparable length and rate with MD-SC-Code-1 (resp., MD-SC-Code-2).

According to Table I, MD-SC-Code-1 has nearly 84% fewer cycles-6 compared to SC-Code-3. Moreover, MD-SC-Code-2 and MD-SC-Code-3 have nearly 73% and 75% fewer cycles-8 compared to SC-Code-4, respectively. As Fig. 2(a) shows, MD-SC-Code-1 has nearly 3.8 orders of magnitude BER performance improvement at SNR= 4.0 dB and 1 dB SNR gain at BER= 10^{-10} compared to its 1D-SC counterpart, i.e., SC-Code-3. As Fig. 2(b) shows, MD coupling (resp., MD coupling along with PP CPO) results in nearly 1.3 (resp., 2.2) orders of magnitude BER performance improvement for MD-SC-Code-2 (resp., MD-SC-Code-3) compared to its 1D-SC counterpart, i.e., SC-Code-4, at SNR= 4.75 dB.

### REFERENCES


